

Amendments to the Specification:

Please replace the paragraph beginning at page 5, line 3 as with the following amended paragraph:

Similarly, while SLEEP 16 is high, inverted SLEEP 16 from inverting buffer 42 is [[lo]] low, thus causing the output 48 of NOR gate 36 to depend on the state of PAD 18. If PAD 18 is high, then the output 48 of NOR gate 36 is low, shutting off controllable weak pull-down 32. When PAD 18 is driven low (for example, by USB Host 22) the output 48 of NOR gate 36 is high, thus turning on controllable weak pull-down 32.